

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application. Please amend claim 1 as follows:

**LISTING OF CLAIMS:**

1. (Currently Amended) A semiconductor light emitting device comprising:

a mesa section having at least a sandwich structure of an n-type clad layer, an active layer and a p-type clad layer which are constituted by compound semiconductor layers formed on a substrate; [[and]]

an insulating film of polyimide covering the mesa section excluding a contact region; and

an inorganic insulating film having a porous area defined by cylindrical vacancies so as to cover the ~~mesa-section~~ insulating film excluding [[a]] the contact region,

wherein the inorganic insulating film has a vacancy rate of 50% or more while being oriented substantially in parallel with a surface of the substrate, and wherein the vacancies are arranged at periodic intervals.

2. (Cancelled).

3. (Previously Presented) The semiconductor light emitting device according to claim 1, wherein the inorganic insulating film comprises a plurality of the porous structures, wherein the cylindrical vacancies are formed such that the

cylindrical vacancies of adjacent porous structures are oriented in different directions.

4. (Previously Presented) The semiconductor light emitting device according to claim 1, wherein the mesa section includes a surface emission structure having an electrode in a top portion and comprises a semiconductor layer provided with an active layer having a quantum well structure constituted by a compound semiconductor, and

a pad to come in contact with the electrode is provided on the inorganic insulating film.

5. (Withdrawn) A method of manufacturing a semiconductor light emitting device including a mesa section have at least a sandwich structure of an n-type clad layer, an active layer and a p-type clad layer which are constituted by compound semiconductor layers formed on a substrate, and an inorganic insulating film formed to cover the mesa section excluding a contact region,

the step of forming the inorganic insulating film comprising:

the step of generating a precursor solution containing a silica derivative and a surface active agent;

the precrosslinking step of raising a temperature of the precursor solution and starting a crosslinking reaction;

the contact step of causing the precursor solution starting the crosslinking reaction at a precrosslinking step to come in contact with a surface of the substrate; and

the step of sintering the substrate with which the precursor solution comes in contact and decomposing and removing the surface active agent, an insulating film being thus formed.

6. (Withdrawn) The method of manufacturing a semiconductor light emitting device according to claim 5, wherein the substrate is dipped in the precursor solution at the contact step.

7. (Withdrawn) The method of manufacturing a semiconductor light emitting device according to claim 5, wherein the substrate is dipped in the precursor solution and is pulled up at a desirable speed in the contact step.

8. (Withdrawn) The method of manufacturing a semiconductor light emitting device according to claim 5, wherein the precursor solution is applied onto the substrate at the contact step.

9. (Withdrawn) The method of manufacturing a semiconductor light emitting device according to claim 8, wherein the contact step is a spin coating step of dropping the precursor solution onto the substrate and rotating the substrate.

10. (Previously Presented) The semiconductor light emitting device according to claim 1, wherein the mesa section includes a surface emission structure having an electrode in a top portion and comprises a semiconductor layer provided

with an active layer having a quantum well structure constituted by a compound semiconductor, and

a pad to come in contact with the electrode is provided on the inorganic insulating film.

11. (Previously Presented) The semiconductor light emitting device according to claim 3, wherein the mesa section includes a surface emission structure having an electrode in a top portion and comprises a semiconductor layer provided with an active layer having a quantum well structure constituted by a compound semiconductor, and

a pad to come in contact with the electrode is provided on the inorganic insulating film.

12. (Previously Presented) The semiconductor light emitting device according to claim 1, wherein the inorganic insulating film is a sintered inorganic insulating film.